

	Type	L #	Hits	Search Text	DBs
2	BRS	L2	17135	first near1 (flip-flop flip adj flop latch latching adj circuit) same second near1 (flip-flop flip adj flop latch latching adj circuit)	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB
3	BRS	L3	410	first near1 (flip-flop flip adj flop latch latching adj circuit) same second near1 (flip-flop flip adj flop latch latching adj circuit) same (logic adj gate\$1 (combination combining combinator\$3) adj circuit)	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB
4	BRS	L4	183	3 and first near1 (flip-flop flip adj flop latch latching adj circuit) same second near1 (flip-flop flip adj flop latch latching adj circuit) same (logic adj gate\$1 (combination combining combinator\$3) adj circuit) same clock	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB